

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	12338	((variable adj2 resist\$6) same transistor)	US-PGPUB; USPAT	OR	ON	2005/09/08 09:20
L2	1	1 and 365/46.ccls.	USPAT	OR	OFF	2005/09/08 09:19
L3	1	1 and 365/46.ccls.	US-PGPUB; USPAT	OR	ON	2005/09/08 09:25
L4	5	1 and 365/100.ccls.	US-PGPUB; USPAT	OR	ON	2005/09/08 09:25
L5	16	1 and 365/148.ccls.	US-PGPUB; USPAT	OR	ON	2005/09/08 09:24
L6	46	((variable adj2 resist\$6) same transistor same (memory or storage))	EPO; JPO	OR	ON	2005/09/08 09:25
L7	37	((variable adj2 resist\$6) same transistor same (memory or storage)).clm.	US-PGPUB	OR	ON	2005/09/08 09:25

*AM checked
 9/8/05
 INTERFERENCE
 SEARCH
 9/8/05
 PMS*